Revision History

0.1 - Initial pre-release
0.2 - Checked by DAB, FJ, JG. Fixed minor mistakes.
0.3 - Removed optic fiber components due to lack of space on the board, added UMAC variant to build list, filled in refdes for build variants.
1.0 - Board rev 1. Includes microwave switch assembly components, DAC SHDN pin tied to +5V instead of GND.
1.1 - Change FB6-11 to use component MZA3216Y102B instead of DLP31.

Build Variants

UMAC chassis compatible
- NOUMAC - No UMAC compatibility
  Do not populate: P1
- UMAC - Draw power from UMAC
  Do not populate: FL1, F13, D28, U27, C46

Parallel I/O
- NOPAR - No Parallel I/O
  Do not populate: J18-21, C39-42, J13, J14
- PARIO - With Parallel I/O
  Do not populate: Parallel I/O

The default configuration is NOUMAC, NOPAR.
Azimuth Digital I/O Connector

Elevation Digital I/O Connector

Digital I/O Grounding

Simulated Incremental Encoder output drivers, protection network

DAC

The DAC controls the Az and El Analog Inputs differentially software writes different values (to each channel pair)

Also, the duty cycle control for the brake drivers are controlled by output channels 5 and 6

Title: Pedestal Interface - Drive Link

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Engineer: JG

Drawn By: JG

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Digital Receiver Link

- \( +3.3V \)
- VCC
- GND
- RX LINK 1
- RX LINK 2
- RX LINK 3
- RX LINK 4
- +3.3V

Line driver can be substituted with a 6SLVDS31 to use LVDS signalling.

Fiber Optic Angle Link

- RX LINK OUT 1
- RX LINK OUT 2
- RX_LINK_OUT_1_P
- RX_LINK_OUT_1_N
- RX_LINK_OUT_2_P
- RX_LINK_OUT_2_N
- RS485_GND

Engineer:

Drawn By:

Title: Pedestal Interface - Receiver Link

Size: Letter
Number: wibex_pi.5
Revision: 1.1

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