Revision History

0.1 - Initial pre-release
0.2 - Checked by DAB, FJ, JG. Fixed minor mistakes
0.3 - Removed optic fiber components due to lack of space on the board, added UMAC variant to build list, filled in fields for build variants
1.0 - Board rev 1. Includes microwave switch assembly components, D/A SHDN pin tied to +5V instead of GND

Build Variants

UMAC chassis compatible

NOUMAC - No UMAC compatibility
Do not populate: P1

UMAC - Draw power from UMAC
Do not populate: F1, F13, D28, U27, C46

Parallel I/O

NOPAR - No Parallel I/O
Do not populate: U18-21, C39-42, J13, J14

PARIO - With Parallel I/O
Do not populate:
Digital Receiver Link

![Digital Receiver Link Diagram]

Fiber Optic Angle Link

![Fiber Optic Angle Link Diagram]

Line driver can be substituted with a 65LVDS31 to use LVDS signalling.

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**Title**: Pedestal Interface - Receiver Link

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**Greeley, CO 80631**

**Engineer**: JG

**Drawn By**: JG

**Date**: 3/24/2009

**Time**: 2:11:25 PM

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**File**: C:\Documents and Settings\jgeorge\My Documents\pdf\pedestinterface-relink.3D.doe
Parallel Angle Input - Elevation

Parallel Angle Input - Azimuth

Pedestal Interface - Parallel Interface